# POZNAN UNIVERSITY OF TECHNOLOGY



EUROPEAN CREDIT TRANSFER AND ACCUMULATION SYSTEM (ECTS) pl. M. Skłodowskiej-Curie 5, 60-965 Poznań

# **COURSE DESCRIPTION CARD - SYLLABUS**

Course name Power Electronics

#### Course

Field of study	Year/Semester
Automatics Control and Robotics	3/5
Area of study (specialization)	Profile of study
	general academic
Level of study	Course offered in
First-cycle studies	Polish
Form of study	Requirements
full-time	elective

#### Number of hours

Lecture 15 Tutorials 0 Number of credit points 3 Laboratory classes 30 Projects/seminars 0 Other (e.g. online) 0

#### Lecturers

Responsible for the course/lecturer: Dariusz Janiszewski Responsible for the course/lecturer:

#### **Prerequisites**

#### **Course objective**

Understanding the basics of electronic components and systems with power electronics. Acquiring the ability to analyze complex and design simple electronic circuits.

Cours	se-	related	learning	outcomes

Knowledge



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The student has a basic knowledge of the principles of measuring electrical quantities, knows and understands the methods of measuring electrical quantities, knows the calculation methods and IT tools necessary to analyze the results of the experiment. - [K1\_W11 (P6S\_WG)]

#### Skills

1. Student is able to use properly selected methods and measuring instruments and measure appropriate signals and on their basis determine the characteristics of electrical systems and obtain information about their essential properties. - [K1\_U15 (P6S\_UW)]

2. The student is able to develop the documentation and present a presentation of the results regarding the implementation of the laboratory task. - [K1\_U03 (P6S\_UK)]

3. The student is able to work individually and in a team; he / she can estimate the time needed to complete the assigned task. - [K1\_U02 (P6S\_UO)]

## Social competences

1. Student understands the non-technical aspects and effects of engineering activities, including its impact on the environment and the related responsibility for decisions. - [K\_K02 (P6S\_KR)]

## Methods for verifying learning outcomes and assessment criteria

Learning outcomes presented above are verified as follows:

Final test, ongoing control of reports and activities during laboratory sessions.

## Programme content

Introduction to transforming power using electronic circuits.

Power electronic elements, power electronics key theory.

Network chargers.

A simple controlled rectifier.

- DC / DC voltage conversion:
- voltage converters,
- voltage boosters,
- reduction / boosting converters,  $\setminus$
- complex multi-stage DC converters.

The idea of converting DC voltage into alternating voltage wave theory.

Single and multi-phase AC voltage converters.

Theory of modulation.



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#### Applications of power electronics

- DC power supplies, including energy ones,
- industrial inverters.

## **Teaching methods**

Lectures: multimedia presentations, blackboard examples

Laboratory: investigation on real and simulated setups

## Bibliography

#### Basic

1. Ned Mohan, Tore M. Undeland, William P. Robins, POWER ELECTRONICS, Converters, Applications and Design, 3-rd edition, Wiley, 2003, 802 pp.

2. Adrian Ioinovici, Power Electronics and Energy Conversion Systems, Volume 1 Fundamentals and Hard-switching Converters, Wiley, 2013

3. M. P. Kazmierkowski, R. Krishnan and F. Blaabjerg (Eds), Control in Power Electronics , Academic Press - USA, 2002, (in English), Author of 4 Chapters 250 pages.

Additional

1. Leszek Frąckowiak, Energoelektronika, cz.2, wyd.5, WPP, Poznań 2003, 354s.

2. S. Januszewski, A. Pytlak, M. Rosnowska-Nowaczyk, H. Świątek, Energoelektronika, WSiP, Warszawa 2004, 296s.

3. Leszek Frąckowiak, Stefan Januszewski, Energoelektronika, cz. 1 ? Półprzewodnikowe przyrządy i moduły energoelektroniczne, WPP, Poznań2001, 166s.

#### Breakdown of average student's workload

	Hours	ECTS
Total workload	75	3,0
Classes requiring direct contact with the teacher	45	2,0
Student's own work (literature studies, preparation for	30	1,0
laboratory classes/tutorials, preparation for tests/exam, project		
preparation) <sup>1</sup>		

<sup>&</sup>lt;sup>1</sup> delete or add other activities as appropriate